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INTERNAT		IAL APPLICATION NO. T/SE98/01931	INTERNATIONAL FILING DATE 27 OCTOBER 1998	PRIORITY DATE CLAIMED  03 NOVEMBER 1997
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Gunnar	BAI	HLENBERG, et al.		
Applicant	here	ewith submits to the United Sta	tes Designated/Elected Office (DO/EO/US) tl	ne following items and other information:
1.	Т	his is a <b>FIRST</b> submission of it	tems concerning a filing under 35 U.S.C. 371	
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4. 🛛	A	proper Demand for Internation	nal Preliminary Examination was made by the	e 19th month from the earliest claimed priority date.
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5. 🗵			(required only if not transmitted by the Inter	national Bureau).
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l		An Information Disclosure Sta	atement under 37 CFR 1.97 and 1.98.	
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**526 Rec'd PCT/PTO 03 MAY** 2000

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2867-188-2 PCT

# IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

:

GUNNAR BAHLENBERG ET AL

: ATTN: APPLICATION DIVISION

SERIAL NO: NEW U.S. PCT APPLICATION

(Based on PCT/SE98/01931)

FILED: HEREWITH

: EXAMINER:

FOR: IMPROVEMENTS IN, OR

**RELATING TO, NEAR-ECHO** 

**SUPPRESSION** 

### PRELIMINARY AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

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Prior to a first examination on the merits, please amend the above-identified

application as follows:

#### IN THE SPECIFICATION

Page 1, before line 1, insert:

#### -- TITLE OF THE INVENTION--;

between lines 1 and 2, insert:

#### -- BACKGROUND OF THE INVENTION

Field of the Invention--;

between lines 4 and 5, insert:

-- Discussion of the Background--;

#### between lines 16 and 17, insert:

#### --SUMMARY OF THE INVENTION---.

Page 2, between lines 18 and 19, insert:

#### BRIEF DESCRIPTION OF THE DRAWINGS ---.

Page 3, before line 1, insert:

# --DESCRIPTION OF THE PREFERRED EMBODIMENTS--.

#### IN THE CLAIMS

Please amend the claims as follows:

Claim 3, line 1, delete "either"; same line, delete "or claim 2,".

Claim 4, line 1, delete "any previous"; same line, after "claim" insert --1--.

Claim 6, line 1, delete "either"; same line, delete "or 5,".

Claim 7, lines 2-3, change "any of claims 1 to 6" to --claim 1--.

Please add new Claims 8-16 as follows:

- --8. A hybrid circuit, as claimed in claim 2, characterized in that said hybrid circuit is adapted to operate with a transmission system employing OFDD, and in that said filter is dimensioned to reject transmit sub-carriers originating from said D/A convertor.
- 9. A hybrid circuit as claimed in claim 2, characterized in that said hybrid circuit is adapted to operate with a duplex system having the following characteristics:

all transmitter in ONUs and NTs in said duplex system are time synchronized;

timing advance is calculated from line lengths;

different sub-carriers are employed for up-stream and down-stream transmissions;

a cyclic prefix is added to compensate for delay propagation in transmission lines; and

frequencies above the FDD band are not employed for longer lines.

10. A hybrid circuit as claimed in claim 3, characterized in that said hybrid circuit is adapted to operate with a duplex system having the following characteristics:

all transmitter in ONUs and NTs in said duplex system are time synchronized; timing advance is calculated from line lengths; different sub-carriers are employed for up-stream and down-stream transmissions;

a cyclic prefix is added to compensate for delay propagation in transmission lines; and

frequencies above the FDD band are not employed for longer lines.

- 11. A hybrid circuit, as claimed in claim 5, characterized in that said balanced hybrid and said filter, together, introduce a delay less than a delay for which said cyclic prefix is dimensioned.
- 12. A duplex transmission system characterized in that said duplex transmission system includes a plurality of hybrid circuits as claimed in claim 2.
- 13. A duplex transmission system characterized in that said duplex transmission system includes a plurality of hybrid circuits as claimed in claim 3.
- 14. A duplex transmission system characterized in that said duplex transmission system includes a plurality of hybrid circuits as claimed in claim 4.
- 15. A duplex transmission system characterized in that said duplex transmission system includes a plurality of hybrid circuits as claimed in claim 5.
- 16. A duplex transmission system characterized in that said duplex transmission system includes a plurality of hybrid circuits as claimed in claim 6.--

#### **REMARKS**

Favorable consideration of this application, as presently amended, is respectfully requested.

The present preliminary amendment is submitted to place the above-identified application in more proper format under United States practice. By the present preliminary amendment the specification has been amended to include suggested headings. The claims have also been amended to no longer recite any multiple dependencies. The subject matter of the cancelled multiple dependencies is also now submitted in new Claims 8-16.

The present application is believed to be in condition for a full and thorough examination on the merits. An early and favorable consideration of the present application is hereby respectfully requested.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

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Attorney of Record

Surinder Sachar

Registration No. 34,423

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PCT/SE98/01931

#### Improvements in, or Relating to, Near-Echo Suppression

The present invention relates to a hybrid circuit for 2-wire to 4-wire conversion in which near-echo is substantially reduced for short lines and to a duplex transmission system employing a plurality of said hybrids.

Hybrid balancing has been used for many years to make 4-wire to 2-wire conversions, and vice versa, for duplex systems using a single line. If the balance is less than ideal, a portion of the transmitted signal will leak through the hybrid into the received signal path. This is referred to as near-echo. If the near-echo is strong, compared to the received signal, more bits are required in an Analogue to Digital (A/D) convertor located in the receive path. The present invention relates to a technique for substantially suppressing near-echo before A/D conversion in 2-ware to 4-wire hybrid circuit.

A hybrid circuit, of the type to which the present invention relates, may be used with the invention described in our co-pending patent application Kgp 152/97, which relates to the application of the present invention to extending the reach of a VDSL.

According to a first aspect of the present invention, there is provided a hybrid circuit having a balanced 2-wire to 4-wire hybrid for interconnecting a two wire receive path and a two wire transmit path to a two wire transmission line, said two wire receive path connecting the balanced hybrid to an A/D convertor and said two wire transmit path connecting a D/A convertor to said balanced hybrid, characterised in that said two wire receive path contains a filter.

Said hybrid circuit may be adapted to operate with a transmission system employing FDD, and said filter may be dimensioned to reject transmit signals originating from said D/A convertor.

Said hybrid circuit may be adapted to operate with a transmission system employing OFDD, and said filter may be dimensioned to reject transmit sub-carriers

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Said hybrid circuit may be adapted to operate with a duplex system having the following characteristics:

- all transmitters in ONUs and NTs in said duplex system are time synchronised;
- timing advance is calculated from line lengths;
- different sub-carriers are employed for up-stream and down-stream transmissions;
- a cyclic prefix is added to compensate for delay propagation in transmission lines; and
- frequencies above the FDD band are not employed for longer lines.

Said cyclic prefix may be dimensioned for lines of length X metres and OFDD is used for lines shorter than X metres.

Said balanced hybrid and said filter, together, may introduce a delay less than a delay for which said cyclic prefix is dimensioned.

According to a second aspect of the present invention, there is provided a duplex transmission system, characterised in that said duplex transmission system includes a plurality of hybrid circuits as described in any previous paragraph.

Embodiments of the invention will now be described, by way of example, with reference to the accompanying drawings, in which:

Figure 1 illustrates, in schematic form, a hybrid circuit according to the present invention.

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In order to facilitate an understanding of the present invention a glossary of terms used in the description of the present invention is provided below:

A/D: Analogue to Digital

ADC: Analogue to Digital Convertor

D/A: Digital to Analogue

DAC: Digital to Analogue Convertor

DMT: Discrete Multi Tone

FDD: Frequency Divided Duplex

NT: Network Termination

OFDD: Orthogonal Frequency Divided Duplex

ONU: Optical Network Unit

VDSL: Very high rate Digital Subscriber Line

Where an A/D convertor is located in the receive arm of a hybrid circuit, as illustrated in Figure 1, the number of bits required in the A/D convertor is determined from the input signal level. If the signal level is increased there will be a loss of resolution when the dynamic range is kept the same. If the near-echo is as strong as the received signal, the A/D convertor will require one extra bit to maintain the same resolution. For long lines, the received signal will be more attenuated than for shorter lines. The near-echo will not be affected by the line length. This means that longer lines will be more affected by the near-echo signal.

The present invention is particularly applicable to reducing near-echo signal

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for the duplex scheme described in our co-pending patent application Kgp 152/97, and DMT symmetric transmission systems of the type described in our patent application PCT/SE 9600935. The basic concept presented in our co-pending application, Kgp 152/97, is the use of Frequency Divided Duplex (FDD) for transmission at lower frequencies and Orthogonal Frequency Divided Duplex (OFDD), also known as Zipper, for transmission at higher frequencies. For long lines only, FDD is used for the lower frequencies (FDD). For short lines, an arbitrary up-/down-stream loading is possible for the higher frequencies. The key elements in the duplex scheme are:

- performance of time synchronisation between all transmitters in the ONU and the NTs;
- calculation of timing advance from the line length;
- use of different sub-carriers in up- and down-stream directions;
- addition of an extension of the cyclic prefix to compensate for delay propagation in the line - this extra cyclic prefix is dimensioned for X metres, where X is the length of the shorter line; and
- not using the frequencies above the FDD band for lines longer than X metres, which means that FDD is used for longer lines and that OFDD can be used for lines less than X m.

To suppress the near-echo signal before A/D conversion, a filter is inserted, see Figure 1. This filter removes the transmitted signal in the FDD band described in our co-pending application Kgp 152/97, in which, where FDD is employed, different frequency bands are used for up- and down-stream bands. This enables filters to be used to separate up-stream bands from down-stream bands. For the ONU side, it will be the FDD downstream band that is filtered out and, for the NT side, it will be the FDD upstream band that is removed.

For long lines, where only the lower frequencies are used, i.e. FDD is

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employed, there is almost no near-echo because of the filter. For shorter lines, where higher frequencies are used, near-echo will be reduced. Suppressing near-echo is more important for long lines where the received signal is more attenuated. To fulfil the orthogonality requirements, the delay of the hybrid plus the filter must be less than the delay for which the extra cyclic prefix is dimensioned.

By using the present invention:

- the number of bits required in the A/D converter, when OFDD is used, is reduced; and
- for longer lines, near-echo is better suppressed.

For the avoidance of doubt the term OFDD, as used in this specification, is intended to embrace similar duplex techniques, such as those employing DMT, wavelet multiplexing, or the like.



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#### **CLAIMS**

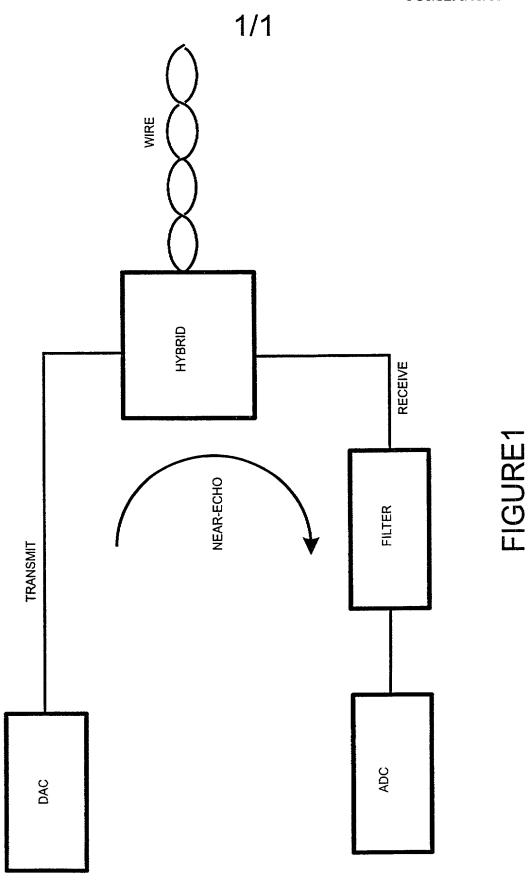
- 1. A hybrid circuit having a balanced 2-wire to 4-wire hybrid for interconnecting a two wire receive path and a two wire transmit path to a two wire transmission line, said two wire receive path connecting the balanced hybrid to an A/D convertor and said two wire transmit path connecting a D/A convertor to said balanced hybrid, and said two wire receive path contains a filter, characterised in that said hybrid circuit is adapted to operate with a transmission system employing FDD at low freequences, and said filter is dimensioned to reject transmit signals originating from said D/A convertor, that said hybrid circuit is adapted to operate with a transmission system employing OFDD at high freequences, and in that said filter is dimensioned to reject transmit sub-carriers originating from said D/A convertor.
- 2. A hybrid circuit as claimed in claim 1, characterised in that said hybrid circuit is adapted to operate with a duplex system having the following characteristics:
  - all transmitters in ONUs and NTs in said duplex system are time synchronised;
  - timing advance is calculated from line lengths;
  - different sub-carriers are employed for up-stream and down-stream transmissions;
  - a cyclic prefix is added to compensate for delay propagation in transmission lines; and
  - frequencies above the FDD band are not employed for longer lines.
- 3. A hybrid circuit as claimed in claim 2, characterised in that said cyclic prefix is dimensioned for lines of length X metres and OFDD is used for lines shorter than X metres.

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- 4. A hybrid circuit, as claimed in either claim 2, or 3, characterised in that said balanced hybrid and said filter, together, introduce a delay less than a delay for which said cyclic prefix is dimensioned.
- 5. A duplex transmission system, characterised in that said duplex transmission system includes a plurality of hybrid circuits as claimed in any of claims 1 to 4.

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# Beclaration, Power Of Attorney and Petition

Page 1 of 7

WE (I) the undersigned inventor(s), hereby declare(s) that:

My residence, post office address and citizenship are as stated below next to my name,

We (I) believe that we are (I am) the original, first, and joint (sole) inventor(s) of the subject matter which is claimed and for which a natent is sought on the invention entitled

IMPROVEMENTS I	N, OR RELATING TO, NEA	R-ECHO SUPPRESSION
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☐ is attac	hed hereto.	
🖸 was file	don May 3, 2000	29
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and am	ended on	•
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Number	PCT/SE98/01931	
	October 27, 1998	<b>&gt;</b>
and was am	ended under PCT Article 19	
оп		(if applicable).

- We (I) hereby state that we (I) have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.
- We (I) acknowledge the duty to disclose information known to be material to the patentability of this application as defined in Section 1.56 of Title 37 Code of Federal Regulations.
- We (I) hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed. Prior Foreign Application(s)

Application No.	Country	Day/Month/Year	Prior Clair	
9704010-9	SWEDEN	3 November 1997	🖾 Yes	□ No
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Page 2 of · 7

	*		Declaration
We (f) hereby claim the benefit une application(s) listed below.	der Title 35, Unite	ed States Code, § 119	(e) of any United States provisional
(Application	Number)	(Filing	Date)
(Application	Number)	(Filing	Date)
We (I) hereby claim the benefit un PCT International application design each of the claims of this application is in the manner provided by the first information which is material to pate filing date of the prior application and	ating the United 5 s not disclosed in 6 st paragraph of 3 ntability as define	States, listed below a the prior United Stat 5 U.S.C. § 112, I d in 37 CFR § 1.56	es or PCT International application acknowledge the duty to disclose which became available between the ing date of this application.
Application Serial No.	Filing C	ate	Status (pending, patented, abandoned)
PCT/SE98/01931	27 October	1998	
And we (I) hereby appoint: Norman McClelland, Reg. No. 21,124; Gregory D. Kelly, Reg. No. 27,757; James D. H. T. Pous, Reg. No. 29,099; Charles L. C. Lavalleye, Reg. No. 31,451; Stephen G. Weihrouch, Reg. No. 32,829; John T. E. Lipman, Reg. No. 30,011; Carl E. S. Neifeld, Reg. No. 35,299; J. Derek M. Gadiano, Reg. No. 37,628; Jeffrey B. M. McCabe, Jr., Reg. No. 37,182; Bradler (my) attorneys, with full powers of subusiness in the Patent Office connects this application be sent to the firm of whose Post Office Address is: Fourth We (I) declare that all statements in made on information and belief are be knowledge that willful false statement under Section 1001 of Title 18 of the	y J. Maier, Reg. No. Jamilton, Reg. No. 26 holz, Reg. No. 26 holz, Reg. No. 30 holz, Reg. No. 31 holz, Reg. No. 32 holz,	o. 25,599; Arthur I. 28,421; Eckhard H. 3,395; William E. Be: 32,884; Richard I. No. 26,142; Richard 34,426; James J. Kul 5,270; Surinder Sacl o. 36,867; William T. To. 40,073; and Mich ocation, to prosecut we (I) hereby reques AK, McCLELLAN erson Davis Highwa r (my) own knowled; and further that the made are punishable de and that such will	Neustadt, Reg. No. 24,854; Richard Kuesters, Reg. No. 28,870; Robert aumont, Reg. No. 30,996; Jean-Paul Treanor, Reg. No. 36,379; Steven P. L. Chinn, Reg. No. 34,305; Steven baski, Reg. No. 34,648; Richard A. ar, Reg. No. 34,423; Christina M. Enos, Reg. No. 33,128; Michael E. ael R. Casey, Reg. No. 40,294; our e this application and to transact all at that all correspondence regarding D, MAIER & NEUSTADT, P.C., y, Arlington, Virginia 22202.  Ige are true and that all statements ese statements were made with the by fine or imprisonment, or both,
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NAME OF FIRST SOLE INVENTO	OR.	Residence: Bli Lulea, SWEI	dvagen 234, S-976 32
Signature of Inventor			WEDEN
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Page 3 of ...7 Declaration

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2000-08-07	

Date

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Page 4 of 7 Declaration

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	Citizen of: SWEDEN
mature of Inventor	Post Office Address:same as above
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ate	
Magnus JOHANSSON	Residence: Timmermansgatan 34.
AME OF SEVENTH JOINT INVENTOR	S-972 51 <u>Lulea.</u> SWEDEN
Magnus Lhanson	Citizen of: _SWEDEN
nature of Inventor	Post Office Address:same as above
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Mauritz LAHTI	Residence: Lingonstigen 63,
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auritz LAHTI	S-973 33 Lulea, SWEDEN  Citizen of: SWEDEN
ME OF EIGHTH JOINT INVENTOR  ME OF EIGHTH JOINT INVENTOR  Mature of Inventor  2000 - 08 - 14	S-973 33 Lulea, SWEDEN  Citizen of: SWEDEN  Post Office Address: same as above  Residence: Praktikantvagen 31,
ME OF EIGHTH JOINT INVENTOR  Machine of Inventor  2000 - 08 - 14	S-973 33 Lulea, SWEDEN  Citizen of: SWEDEN  Post Office Address: same as above  Residence: Praktikantvagen 31,
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	Hans LUNDRERG	Recidence: V	<i>]</i> astra	Solgatan	8.
10W	Hans LUNDBERG NAME OF SIXY HOINT INVENTOR			3 Lulea, S	
	TENTH				SEX
	1000	Citizen of:	SWEDE	Ŋ	,
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	√ 2000-08-10 Date				
11/1)	Tomas NORDSTROM NAME OF SEVENTH JOINT INVENTOR	Residence: _	Docent	tvagen 279 52 Lulea,	ewenen
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	The Market	Citizen of: _	SWEDE	N	
	Signature of Inventor	Post Office		same as a	above
, Ag	₹ 2000 - 08 - 28 Date			20	
20	Lennart OLSSON NAME OF EXCHURA JOINT INVENTOR	Residence: _		gen 39, 31 Lulea,	, SWEDEN
9	TWELTH				SEX
	Lamant Olsson	Citizen of:	SWEDE		
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130	Sven-Rune OLOFSSON	Residence:	Malmu	ldsvagen !	9,
12	NAME OF NEWE JOINT INVENTOR THIRTEENTH		S-972	46 Lulea	、SWEDEN
	SR Gaf	Citizen of:	SWEDE	NI.	SEX
	Signature of Inventor	Post Office		came ac	above
	1 2000-08-14				
	Date				

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Page 6 of 7 Declaration

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NAME OF STATE JOINT INVENTOR FOURTEENTH	S-961 93 Boden, SWEDEN
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Signature of Inventor	Post Office Address: same as above
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Van Ohman	Citizen of: SWEDEN
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14.	Chierra
×	Citizen of: SWEDEN
Signature of Inventor	Post Office Address: same as above
· 2 mm 07-31	
2000-07-31 Date	- GLD!
Date	OLD!  Residence: Professorsvagen 108 B,
Per ODLING NAME OF NUMBER JOINT INVENTOR	Residence: Professorsvagen 108 B,
Date Per ODLING	
Per ODLING NAME OF NUMBER JOINT INVENTOR	Residence: Professorsvagen 108 B,  S-97/ 51 Luled, SWEDEN  PL 680C LAKASUND, 5-89/96 ARNA  SWED
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\ Petra DENTGEN	Residence: Bocentvagen 141,
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EIGHTEENTH	* 23 Retober 2000 PD. SCY
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NAME OF MEXICAL DOLL IN THE TENTH	SE
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Signature of Inventor	Post Office Address: same as above
교 usa 면 ( : Bi 문 ( : Bi	Post Office Address:
2000-07-31	
Date	
	Residence:
NAME OF EIGHTH JOINT INVENTOR	
	Citizen of:
Signature of Inventor	Post Office Address:
Date	
	Residence:
NAME OF NINTH JOINT INVENTOR	
Signature of Inventor	Citizen of:  Post Office Address:
Vigament	Post Office Address: